

**Bhoj Reddy Engineering College for Women**  
**Department of Electronics & Communication Engineering**  
**Time Table for the Academic Year 2015-16**  
**II B.Tech II Semester ECE Section-A**

Room No : 203 North Block (NB)

w.e.f : 21-12-2015

Day	930-1030	1030-1130	1130 - 1230	1230 - 1330	1330 - 1430	1430-1530	1530-1630
Monday	ECA	EMTL	PEE	LUNCH	EMTL(G1-203) ECA(G2-301) PDC (G3-302)	DDV	ES
Tuesday	GS	PDC	ECA		ES	ECA(G1-203) PDC(G2-301) EMTL(G3-302)	EMTL
Wednesday	PDC	DDV	ES		PEE	<-----GS LAB----->	
Thursday	<-----ECPC/ ET LAB----->				PEE	EMTL	PDC
Friday	<----- ET/ ECPC LAB----->				ECA	ES	DDV
Saturday	EMTL	DDV	PDC(G1-203) EMTL (G2-301) ECA(G3-302)		PDC	ECA	PEE

S.No	Subject		Faculty	Dept
1	PEE	Principles of Electrical Engineering	T.Vinay Kumar	EEE
2	ECA	Electronic Circuit Analysis	K.Chandrashekhara Reddy	ECE
3	PDC	Pulse and Digital Circuits	B.Prabhavathi	ECE
4	ES	Environmental Studies	Y. Swetha	BS
5	EMTL	Electromagnetic Theory and Transmission Lines	P.A.Sravanthi	ECE
6	DDV	Digital Design using Verilog HDL	S.Manjula	ECE
7	GS	Gender Sensitization	J. Sujatha	ENGLISH
8	ET LAB	Electrical Technology Lab	T.Vinay Kumar, B.Laxman/SK. Vali	EEE
9	ECPC LAB	Electronic Circuits and Pulse Circuits lab	B.Prabhavathi, K.Chandrashekhara Reddy	ECE
10	GS LAB	Gender Sensitization Lab	J. Sujatha	ENGLISH

Tutorial Groups :

G1: 401-424

G2: 425-448

G3: 449-L412